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# Single-ended amplifier-based touch readout circuit with immunity to display noise

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## ABSTRACT

To suppress errors in a touch readout circuit due to display noise, differential sensing using fully differential amplifiers is widely used. However, conventional differential sensing methods require additional circuits or increase circuit complexity, thus increasing power consumption and circuit area or requiring additional dummy RX line on the touch panel. In this study, we propose a compact touch readout circuit composed of a single-ended amplifier while keeping the display noise suppression of differential sensing so as to minimize power consumption and circuit area increase and avoid the need for additional dummy RX line. The proposed touch readout circuit for is fabricated in circuit area of 0.4 mm<sup>2</sup> with 0.35 μm 3.3 V CMOS process and measured for 10.1-inch touch panel on top of a TFT-LCD panel. With the proposed touch readout circuit, the signal-to-noise ratio (SNR) is improved by up to 13.1 dB compared to the conventional touch readout circuit using single-ended amplifiers.

## ARTICLE HISTORY

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## KEYWORDS

Readout circuit; mutual capacitive touch; display noise suppression

## SUBJECT CLASSIFICATION CODES

LC; OLED; equipment

## 1. Introduction

The touch readout circuit, which reads out the touch signal from the capacitive touch panel, must be robust to display noise, because large display noise is injected from the operating display panel [1–7]. In particular, as the add-on type touch panel has advanced into an on-cell and further, an in-cell type touch panel, the gap between the display panel and the touch panel has been greatly decreased, and the display noise injected into the touch panel has become a more serious problem [3–10]. As shown in Figure 1, display noise is injected to the RX electrode of the touch panel through the common electrode located at the top of the display panel (for example, the cathode electrode in a top-emission OLED panel). Therefore, display noise is considered a common noise independent of the position in the panel [1,3–5,8,11,12]. Therefore, the display noise can be effectively suppressed by using a differential sensing method [1–3,6,11–13]. There are two common circuit implementations to realize the differential sensing method. One is to additionally use a fully differential amplifier that receives the outputs of the pair of readout amplifiers of two adjacent channels as differential inputs [6,12]. However, the added circuits increase the power consumption and the circuit area. The other is to design each channel's readout amplifier as a fully differential amplifier [1–3,11,13]. This

circuit, however, can only detect the difference in capacitance between the adjacent two channels, then it is not sufficient for touch detection. To determine a touch, an additional dummy RX line is needed as the reference for the touch panel.



In this study, we propose a compact touch readout circuit that consists of a single-ended amplifiers instead of fully differential amplifiers, but at the same time effectively suppresses errors in a touch readout circuit due to display noise. The proposed compact touch readout circuit neither increases in circuit area or power consumption nor requires additional dummy RX lines.

The rest of this paper is organized as follows: Section 2 describes the principle of the proposed touch readout circuit. Section 3 shows the simulation and measurement results. This paper is concluded in Section 4.

## 2. Proposed touch readout circuit

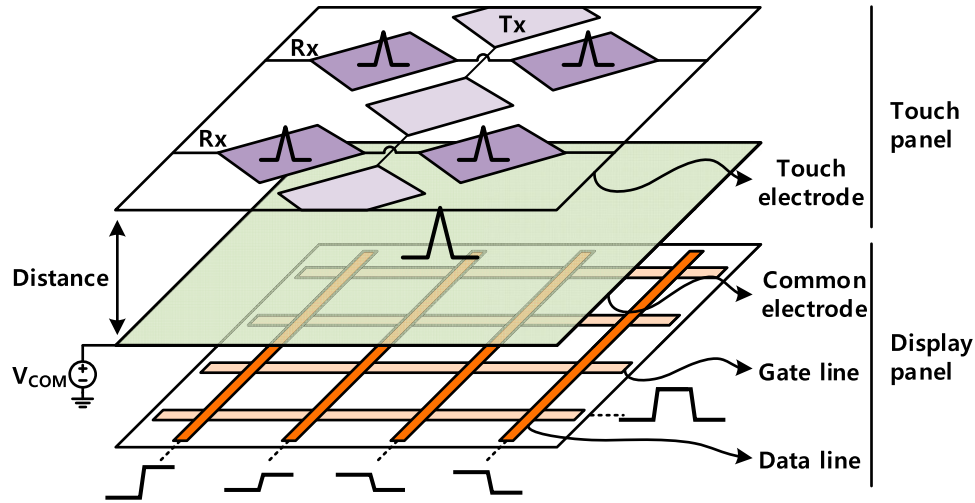
### 2.1. Principle of the proposed touch readout circuit

Figure 2 shows the effect of display noise on the conventional touch readout circuit composed of single-ended amplifiers. In Figure 2,  $C_M[N]$  represents the mutual capacitance between the RX line of the N-th channel and a TX line running perpendicular to the RX line.  $C_D$  is the parasitic capacitance between the RX line and the

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**Figure 1.** Display noise injection to the touch panel.

common electrode.  $C_P$  represents all the other parasitic capacitances between the RX line and the other electrodes. In the conventional single-ended amplifier-based touch readout circuit, the positive input of each amplifier is connected to the DC voltage,  $V_{REF}$ , so the display noise introduced through the common electrode appears at the output of the touch readout circuit, as shown in Figure 2.

To suppress the display noise, fully differential amplifiers are widely used to suppress the display noise, and the display noise is injected equally into both the positive and negative inputs so that it does not appear at the output of the touch readout circuit by common-mode rejection. In this study, we propose the use of compact single-ended amplifiers in which the display noise is still injected into both the positive and negative inputs of each single-ended amplifier. This allows a touch readout circuit to be constructed with single-ended amplifiers, which is relatively simple compared to fully differential amplifiers, while suppressing the display noise at the output of the touch readout circuit. The concept of the proposed touch readout circuit is shown in Figure 3, where the positive input of any amplifier except for the first channel's amplifier is connected to the RX line of the adjacent channel. The first channel's amplifier must have its positive input connected to a DC voltage. Otherwise, the positive input of any amplifier is undetermined because the voltage of the RX lines is undefined. In the proposed touch readout circuit configuration, two adjacent RX lines are connected to the positive and negative inputs of the amplifier, respectively, so the display noise is commonly injected into the two inputs. As mentioned above, because the display noise is independent of the position in the touch panel, the noise applied on the two RX lines are considered the

same. Therefore, the display noise does not appear at the output of the touch readout circuit, because common-mode rejection is also possible even with a single-ended amplifier.

## 2.2. Analysis and design of the proposed touch readout circuit

The display noise suppression of the proposed touch readout circuit can be analyzed as follows: The voltage change of common electrode is denoted as  $\Delta V_{COM}$ . And the voltage changes of  $OUT[i]$  and  $RX[i]$  ( $1 \leq i \leq N$ ) due to the  $\Delta V_{COM}$  are denoted as  $\Delta V_{OUT[i]}$  and  $\Delta V_{RX[i]}$ , respectively. Even if  $\Delta V_{COM}$  injects charges into  $RX[i]$ , if the amplifier ideally has the infinite open-loop gain, the amplifier will make the voltage of the negative input equal to the voltage of the positive input. That is to say, ideally, the amplifier cancels out the injected charges and makes  $\Delta V_{RX[i]}$  zero. However, since  $\Delta V_{RX[i]}$  is non-zero due to the finite open-loop gain of the amplifier, charge change at  $RX[i]$  due to  $\Delta V_{COM}$  can be expressed as in (1).

$$C_F(\Delta V_{OUT[i]} - \Delta V_{RX[i]}) + (C_M + C_P)(-\Delta V_{RX[i]}) + C_D(\Delta V_{COM} - \Delta V_{RX[i]}) = 0 \quad (1)$$

In (1), the display noise through TX lines is not considered because TX lines are driven by TX drivers. This assumption is found in many literatures, for example [2,4,11]. As shown in Figure 3, in the proposed touch readout circuit, the positive input of the amplifier in the first channel is fixed at a DC voltage,  $V_{REF}$ . If  $V_{OUT}[1]_0$  and  $V_{RX}[1]_0$  are defined as the voltages before  $\Delta V_{COM}$  occurs,  $\Delta V_{OUT}[1]$  and  $\Delta V_{RX}[N]$  are defined as the voltage changes due to  $\Delta V_{COM}$ , and the open-loop gain of

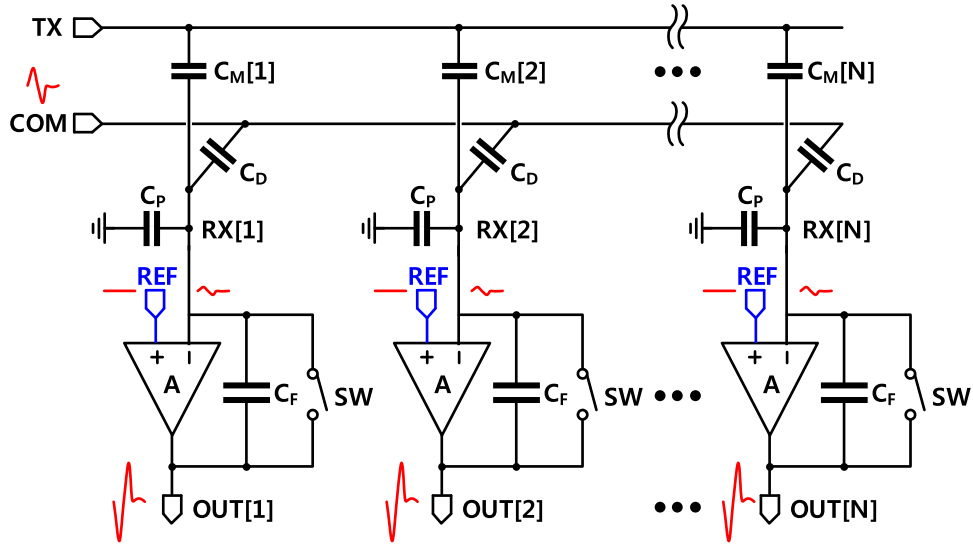


Figure 2. Display noise in the conventional single-ended amplifier-based touch readout circuit.

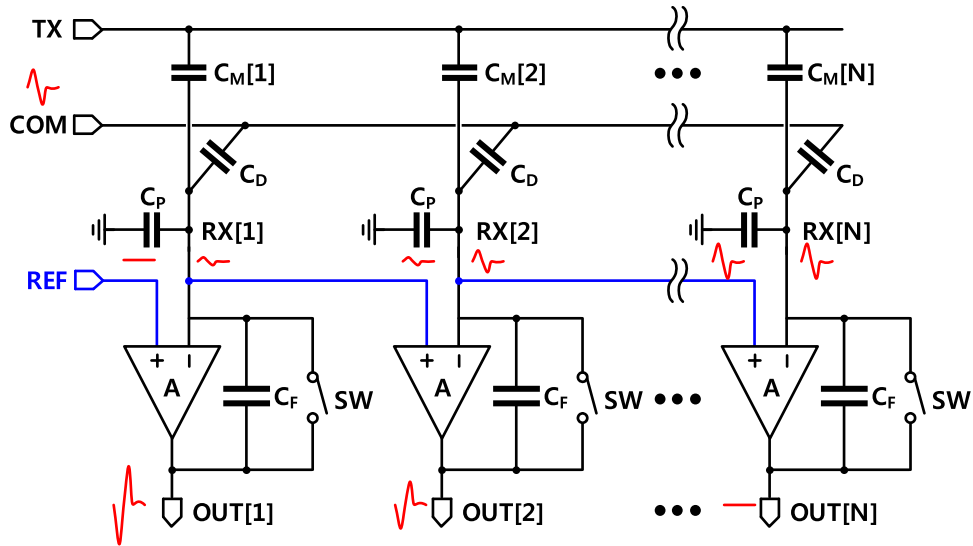


Figure 3. Proposed single-ended amplifier-based touch readout circuit.

the amplifiers are denoted as  $A$ ,  $\Delta V_{OUT} [1]$  is obtained as (2).

$$\begin{aligned} \Delta V_{OUT[1]} &= V_{OUT[1]} - V_{OUT[1]0} \\ &= A(V_{REF} - V_{RX[1]}) - A(V_{REF} - V_{RX[1]0}) \\ &= -A(V_{RX[1]} - V_{RX[1]0}) = -A\Delta V_{RX[1]} \end{aligned} \quad (2)$$

If we put (2) into (1) and let  $i = 1$ ,  $\Delta V_{RX} [1]$  and  $\Delta V_{OUT} [1]$  of the first channel are obtained as (3) and (4), respectively.

$$\Delta V_{RX[1]} = \frac{C_D}{(A + 1)C_F + C_D + C_M + C_P} \Delta V_{COM} \quad (3)$$

$$\Delta V_{OUT[1]} = -\frac{AC_D}{(A + 1)C_F + C_D + C_M + C_P} \Delta V_{COM} \quad (4)$$

On the other hand, the positive input of the amplifier in the second channel in Figure 3 is connected to  $RX[1]$ , which is the  $RX$  line of the first channel. The output voltage of the second channel,  $V_{OUT[2]}$  and  $\Delta V_{OUT[2]}$  are obtained as (5) and (6), respectively.

$$\begin{aligned} V_{OUT[2]} &= A(V_{RX[1]} - V_{RX[2]}) \\ \Delta V_{OUT[2]} &= V_{OUT[2]} - V_{OUT[2]0} \\ &= A(V_{RX[1]} - V_{RX[2]}) \\ &\quad - A(V_{RX[1]0} - V_{RX[2]0}) \end{aligned} \quad (5)$$

$$\begin{aligned}
&= A[(V_{RX[1]} - V_{RX[1]0}) \\
&\quad - (V_{RX[2]} - V_{RX[2]0})] \\
&= A(\Delta V_{RX[1]} - \Delta V_{RX[2]}) \quad (6)
\end{aligned}$$

If we put (6) into (1) and let  $i = 2$ ,  $\Delta V_{RX[2]}$  is obtained as (7), and if we put  $\Delta V_{RX[1]}$  in (3) into (7),  $\Delta V_{RX[2]}$  is simplified as (8).

$$\begin{aligned}
\Delta V_{RX[2]} &= \alpha \Delta V_{RX[1]} \\
&\quad + \frac{C_D}{(A+1)C_F + C_D + C_M + C_P} \Delta V_{COM} \quad (7)
\end{aligned}$$

where  $\alpha = \frac{AC_F}{(A+1)C_F + C_D + C_M + C_P}$ .

$$\begin{aligned}
\Delta V_{RX[2]} &= (1 + \alpha) \frac{C_D}{(A+1)C_F + C_D + C_M + C_P} \\
&\quad \times \Delta V_{COM} \quad (8)
\end{aligned}$$

In the same way as in (7),  $\Delta V_{RX[3]}$  in the third channel is obtained as (9), and if we put (8) into (9),  $\Delta V_{RX[3]}$  is expressed as (10).

$$\begin{aligned}
\Delta V_{RX[3]} &= \alpha \Delta V_{RX[2]} + \frac{C_D}{(A+1)C_F + C_D + C_M + C_P} \\
&\quad \times \Delta V_{COM} \quad (9) \\
\Delta V_{RX[3]} &= (1 + \alpha + \alpha^2) \frac{C_D}{(A+1)C_F + C_D + C_M + C_P} \\
&\quad \times \Delta V_{COM} \quad (10)
\end{aligned}$$

In the same way, the  $(N-1)$ -th channel and the  $N$ -th channel,  $\Delta V_{RX[N-1]}$  and  $\Delta V_{RX[N]}$  are obtained as (11) and (12), respectively.

$$\begin{aligned}
\Delta V_{RX[N-1]} &= (1 + \alpha + \alpha^2 + \dots + \alpha^{N-2}) \\
&\quad \times \frac{C_D}{(A+1)C_F + C_D + C_M + C_P} \Delta V_{COM} \quad (11)
\end{aligned}$$

$$\begin{aligned}
\Delta V_{RX[N]} &= (1 + \alpha + \alpha^2 + \dots + \alpha^{N-1}) \\
&\quad \times \frac{C_D}{(A+1)C_F + C_D + C_M + C_P} \Delta V_{COM} \quad (12)
\end{aligned}$$

$\Delta V_{OUT[N]}$  of the  $N$ -th channel can be obtained as (13) in the same way as (6), and if we put  $\Delta V_{RX[N-1]}$  in (11) and  $\Delta V_{RX[N]}$  in (12) into (13),  $\Delta V_{OUT[N]}$  is expressed as (14).

$$\begin{aligned}
\Delta V_{OUT[N]} &= A(\Delta V_{RX[N-1]} - \Delta V_{RX[N]}) \quad (13) \\
\Delta V_{OUT[N]} &= -\alpha^{N-1} \frac{AC_D}{(A+1)C_F + C_D + C_M + C_P}
\end{aligned}$$

$$\begin{aligned}
&\quad \times \Delta V_{COM} \\
&= -\alpha^N \frac{C_D}{C_F} \Delta V_{COM} \quad (14)
\end{aligned}$$

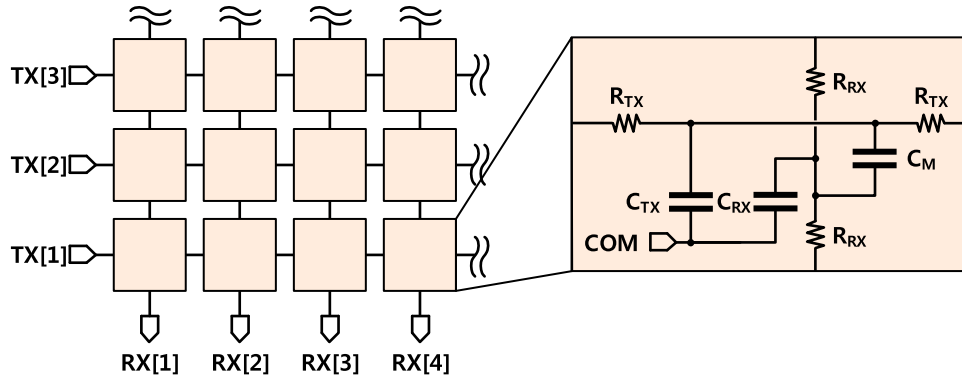
If the open-loop gain of the amplifier ( $A$ ) for  $\Delta V_{COM}$  is not much high,  $\alpha$  in (14) is smaller than one and is multiplied to the power of  $N$ , which suppresses the display noise. Whereas, in the case of conventional single-ended amplifier-based readout circuit,  $\Delta V_{OUT}$  for all channels is the same as when  $N = 1$  in (14),  $\Delta V_{OUT}$  for  $\Delta V_{COM}$  of the proposed circuit decreases with  $N$ . On the other hand,  $\Delta V_{OUT}$  for  $\Delta V_{TX}$  can be easily obtained from (14) by substituting the injected charge due to display noise ( $C_D \Delta V_{COM}$ ) with the injected charge due to  $\Delta V_{TX}$  ( $C_M \Delta V_{TX}$ ). Since the frequency of the  $\Delta V_{TX}$  is higher than  $\Delta V_{COM}$ , the open-loop gain of the amplifier ( $A$ ) for  $\Delta V_{TX}$  is higher than the open-loop gain ( $A$ ) for  $\Delta V_{COM}$ . If  $A$  is sufficiently high,  $\alpha$  for  $\Delta V_{TX}$  is close to one, and the signal due to  $\Delta V_{TX}$  is not suppressed. Meanwhile,  $\Delta V_{OUT}$  for  $\Delta V_{TX}$  of the proposed circuit which is the touch signal is similar to that of the conventional circuit, thus the output of the proposed circuit depends on the mutual-capacitance ( $C_M$ ), not the difference of the mutual-capacitances of the adjacent RX lines.

Since  $\Delta V_{OUT}$  for  $\Delta V_{COM}$  of the proposed circuit decreases with  $N$ , this means that the display noise suppression is effective for channels far from the first channel but might be insufficient for channels close to the first channel. To address this problem, the proposed touch readout circuit uses a bidirectional scanning method, where a frame is divided into two subframes and reverses scanning direction between the subframes. More specifically, in the first subframe,  $V_{REF}$  is connected to the first channel (light-green in Figure 4), and the outputs of channels farther away from the first channel than the  $(N/2)$ -th channel are used as valid outputs of the touch readout circuit. Conversely, in the second subframe,  $V_{REF}$  is connected to the  $N$ -th channel which is the last channel (light-blue in Figure 4), and  $RX[i+1]$  is connected to the positive input of the amplifier in the  $i$ -th channel, so that the output of the  $(N/2)$ -th channel and the outputs of the channels closer to the first channel than the  $(N/2)$ -th channel are used as valid outputs of the touch readout circuit. Throughout the first and second subframes, all outputs of the touch readout circuits can be obtained with the display noise suppressed.

### 3. Simulation and measurement results

Display noise suppression of the proposed touch readout circuit was measured with Lenovo's 10.1-inch add-on type mutual capacitive touch panel. Also, for SPICE simulation, the circuit model of the touch panel is configured





**Figure 5.** Touch panel circuit model for simulation [5,14].

**Table 2.** Designed single-ended two-stage amplifier specifications.

Item	Value	Item	Value
DC Gain	80 dB ~	Bandwidth	5 MHz ~
Phase Margin	60° ~	Offset Voltage	~ 0.4 mV
Quiescent current	42 $\mu$ A	Area	3600 $\mu$ m <sup>2</sup>

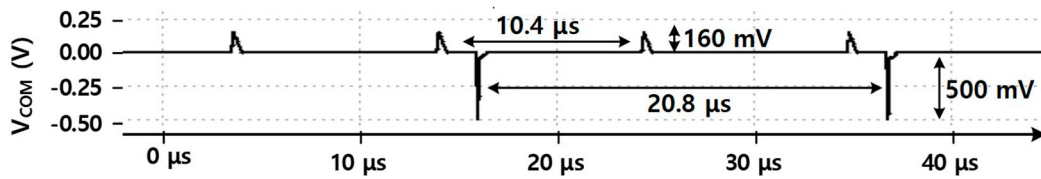
where  $V_{OUT\_TOUCH}[i]$  is the output voltage of  $i$ -th measurement when touched, and  $\overline{V_{OUT\_TOUCH}}$  and  $\overline{V_{OUT\_w/oTOUCH}}$  are the average of the output voltage of each channel for 100 measurements when touched and not touched, respectively.

In Figures 8, the SNR of the proposed readout circuit for the full white and the zebra-pattern screens, both simulated and measured, are presented. Figure 8(a,b) shows the SNR in the first and second subframe, respectively. In the first subframe,  $V_{REF}$  is connected to the first channel, and in the second subframe, to the last channel (14th channel). In Figure 8(a), the first channel has the lowest SNR because it has a fixed positive input to  $V_{REF}$  in the same way as the conventional single-ended amplifier-based touch readout circuit, but the SNR increases as the channel becomes farther away from the first channel. The SNR for the full-white screen is measured to increase up to 13.1 dB at the last channel compared to the first channel and up to 12.6 dB for the zebra pattern screen. Therefore, in the first subframe, we only accept the outputs of the eighth to 14th channels as valid touch information. In Figure 8(b), on the same principle, the measured SNR increases up to 13.1 and 12.7 dB compared to the last channel for the full-white and the zebra pattern screens, respectively. This is because, in the second subframe, the amplifier of the 14th channel has its positive input fixed at  $V_{REF}$ . In the second subframe, we only accept the outputs of the first to seventh channels as valid touch information. Finally, by collecting the touch information from two subframes, the proposed touch readout circuit improves the SNR by a minimum

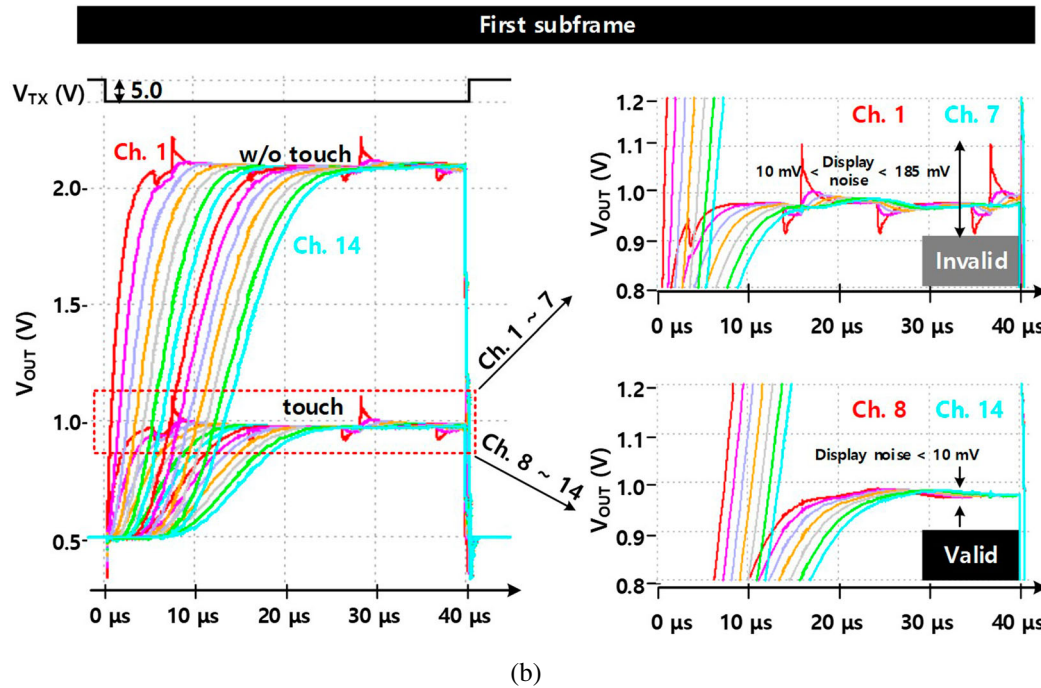
of 6.3 dB to a maximum of 13.1 dB for the full-white screen and by a minimum of 7.2 dB to a maximum of 12.6 dB for the zebra pattern screen. The slightly lower SNR for the zebra pattern screen than that for the full-white screen is because the measured display noise for the zebra pattern screen is a maximum of 550 mV, while the measured display noise for the full-white screen is maximum of 500 mV. A 10% larger noise reduces the SNR by 0.8 dB in calculations, which is in good agreement with the measurement results. In addition, the measured SNRs in Figure 8 are lower than the simulated SNRs. This is because the simulations only include display noise, while the measurements also include internal circuit noise, power noise, jitter of the control signal, and other environmental noise. The SNR of the fabricated proposed touch readout circuit is compared with the previous works in Table 3. Given the fact that all the previous works in Table 3 use fully differential amplifiers to reject display noise, the proposed single-ended amplifier-based touch readout circuit exhibits good SNR, although not the best. Due to the simple circuit structure of the proposed touch readout circuit, the circuit area and power consumption are much lower than those of the previous works.

#### 4. Conclusion

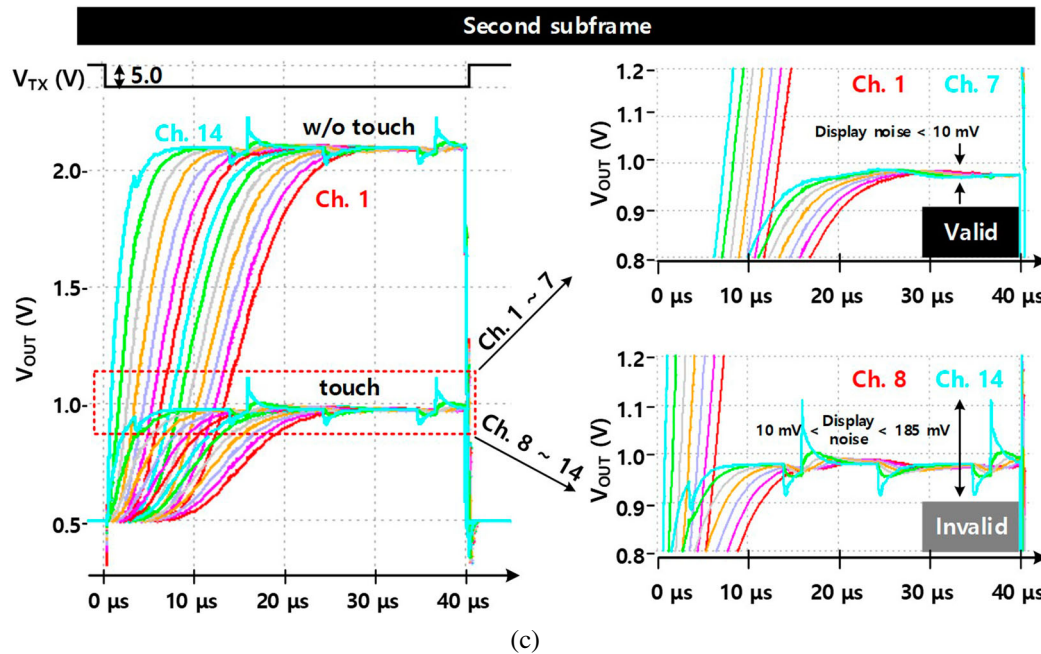
A touch readout circuit composed of single-ended amplifiers that effectively suppresses display noise is proposed, where the positive input of each amplifier is connected to the RX line of the adjacent channel instead of a fixed reference voltage and the negative input is connected to the RX line of its own channel. As a result, display noise is injected equally into both the positive and negative inputs of the amplifier and suppressed at the output of the touch readout circuit by common-mode rejection of the single-ended amplifier. Compared with fully differential amplifier-based touch readout circuits,



(a)

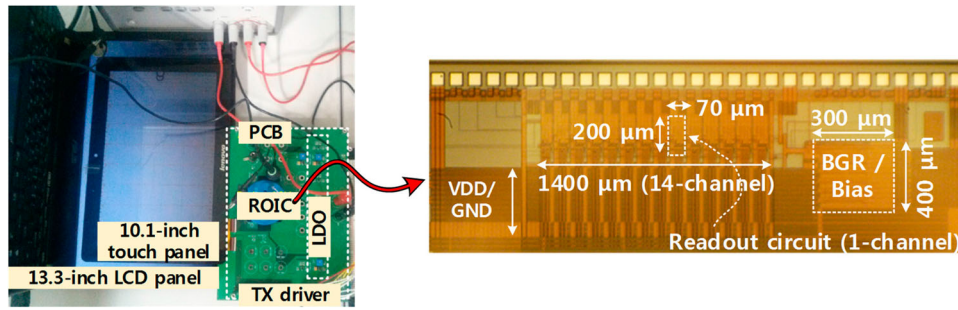


(b)

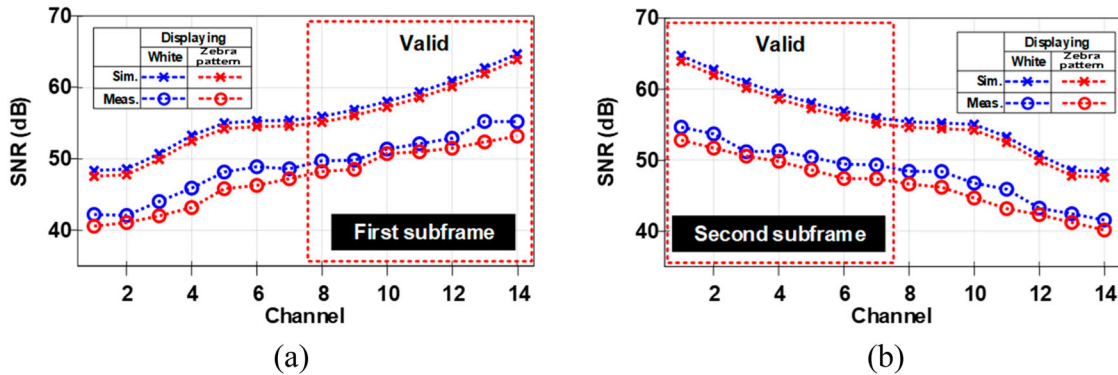


(c)

Figure 6. Simulation results of the proposed touch readout circuit. (a) Display noise. (b) First subframe. (c) Second subframe.



**Figure 7.** Micrograph of the fabricated touch readout circuit and measurement setup.



**Figure 8.** Simulated and measured results of the proposed touch readout circuit. (a) SNR of the first subframe. (b) SNR of the second subframe.

**Table 3.** Comparison with the previous works.

	This work	[16]	[13]	[2]	[3]	[6]
Readout structure	Single-ended	Fully differential	Fully differential	Fully differential	Fully differential	Additional fully differential
Touch panel	10.1-inch mutual	5.3-inch mutual	7.9-inch mutual	6.9-inch mutual	5-inch self/mutual	4.3-inch mutual
Channel	TX = 43 RX = 14	TX = 31 RX = 15	TX = 14 RX = 16	TX = 12 RX = 16	TX = 28 RX = 16	TX = 12 RX = 8
SNR (dB)	47.2 ~ 56.2	54.2	52.9	27.5	60 (mutual) 53 (self)	60
Framerate(Hz)	240	240	367	175	120	200
Power*( $\mu$ W/ch.)	450**	766	825 (analog) 1,031 (digital)	4,750	638 (analog) 900 (digital)	788
Area* (mm <sup>2</sup> /ch.)	0.029**	0.099	0.353	0.314	0.994	0.275
Process	0.35 $\mu$ m CMOS	0.13 $\mu$ m CMOS	0.35 $\mu$ m CMOS	0.35 $\mu$ m CMOS	90 nm CMOS	0.18 $\mu$ m CMOS

\*Total power and circuit area are divided by number of RX channels.

\*\*Only for analog front-end.

which are commonly used to reject display noise, the proposed touch readout circuit consumes much lower chip and power consumption due to its simple circuit structure.

The proposed touch readout circuit was fabricated in a 0.35  $\mu$ m CMOS process and measured to have SNR of 47.2 to 56.2 dB, which is up to 13.1 dB improved compared to the conventional single-ended amplifier-based touch readout circuit. Even compared with the reported fully differential amplifier-based touch readout circuits, the SNR is comparable with much lower circuit area (0.029 mm<sup>2</sup> per RX channel) and power (450  $\mu$ W per RX channel) consumed.

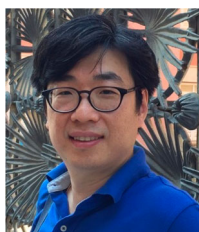
## Disclosure statement

No potential conflict of interest was reported by the author(s).

## Notes on contributors



*Yongsang Yoo* received the B.S. and M.S. degrees in electronic engineering from Hanyang University, Seoul, South Korea, in 2014 and 2016, respectively, where he is currently pursuing the Ph.D. degree. His research interests include sensor readout circuits, memory core, and peripheral circuits.



**Byong-Deok Choi** (Member, IEEE) received the B.S., M.S., and Ph.D. degrees in electronic engineering from Hanyang University, Seoul, South Korea, in 1994, 1996, and 2002, respectively. Since 2005, he has been with Hanyang University, where he is currently a Professor with the Department of Electronic Engineering. His current research interests include power management IC, high-voltage power devices and circuits, analog and low-power circuits, driver ICs for displays, and hardware security.

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